

FORWARDING LOAD DATA TO YOUNGER INSTRUCTIONS IN ANNEX

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ABSTRACT

5 A method and computer system for bypassing load data to younger instructions are
provided. The method and computer system contemplate a microprocessor that manages
interlock conditions for load instructions and that supports speculative and out-of-order
execution of load instructions. A freshest value for a source register is located among the entries
of a load annex and one or more non-load annexes. The contents of the freshest value is
10 bypassed to a current instruction for execution.